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# Single-phase surge voltage protectors with interference suppression filter **SFS** series

#### Description:

Surge voltage protectors with interference suppression filter SFS series are intended for protection from the effects of impulse overvoltage and radiofrequency interference incoming along LV line.

They function as overvoltage protection of type D. The interference suppression filter is formed from low pass filter 0 -10kHz and it restricts radio-frequency interference in the 0,1-100 MHz range and also it effectively suppresses very rapid impulses.

Contacts S1, S2 serve for indication of overloading of the surge voltage protector. When it comes to opening of contact between S1 and S2, the surge voltage protector will not be functional.

It is designed for an assembly onto a DIN rail into switchboards or straight into the appliance.



#### TECHNICAL PARAMETERS:

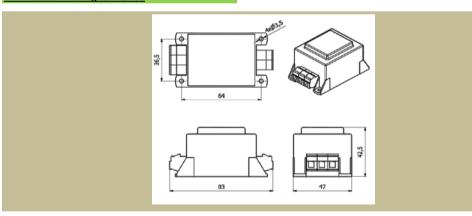
Nominal operating voltage Extent of operating currents Nominal impulse current (L-N) , (L,N-PE) 8/20us Short-term overcurrent capacity: 50% In Thermal class: B Protection class: IP20

110Vac / 150Vdc 10A 6,5kA

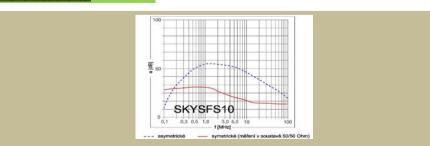
Туре	Nominal current [A]	Leakage current 1*) [mA]	Weight [kg]	basic dimensions [mm]					
				length	height	width	pitch	pitch	other
SKYSFS10	10	-	0,1	80	27,6	68	47,5	38,5	-

<sup>\*</sup> After a deal there is a possibility of modification of the filter construction according to the customer's request.

### **Dimensional drawing: SKYSFS3**

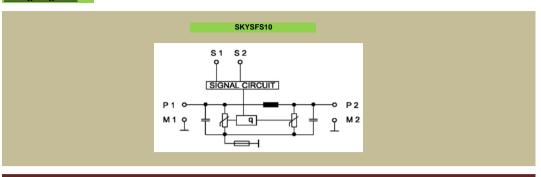


#### **Attenuation characteristics:**



<sup>1\*)</sup> Leakage current measurement was performed according to the standard ČSN EN 60950.

## Wiring diagram :



#### Use

They are used for appliances which we want to protect from impulse overvoltage and radiofrequency interference.

#### <u>Dimensioning, wiring:</u>

They are dimensioned according to the indicated label nominal voltage and current values. Short-circuit protection must not exceed nominal current value. When connecting it is necessary to meet the EMC requirements. There must not be any paralleling of interference-suppressed and non-interference-suppressed circuits. The grounding connections must be as short as possible.